



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajesh Tiwari, et al.

Docket No: TI-36211

Serial No: 10/663,948

Conf. No: 3857

Examiner: Phat X. Cao

Art Unit: 2814

Filed: 09/16/2003

For: DUAL DEPTH TRENCH TERMINATION METHOD FOR IMPROVING CU-BASED
INTERCONNECT INTEGRITY

LETTER TO OFFICIAL DRAFTSPERSON

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Official Draftsperson

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)
I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service with sufficient
postage as First Class Mail in an envelope addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA
22313-1450 on 6-15-05.

Ann Trent
Ann Trent

Dear Sir:

Transmitted herewith for filing are four sheets of formal drawings for the above
identified application. Charge any necessary fees to the deposit account of Texas
Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

Peter K. McLarty
Attorney for Applicants
Reg. No. 44,923

Texas Instruments Incorporated
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